Peraso Announces Knowledge Resources Has Chosen
BE-3 Memory IC for FPGA Acceleration

San Jose, CA – June 23, 2022 – Peraso Inc. (NASDAQ:PRSO) (Peraso®), a leader in mmWave technology, today announced that Knowledge Resources GmbH, a sophisticated Electronics Design Services company located in Basel, Switzerland, is now using Peraso's Bandwidth Engine 3 (BE-3) memory solution for its latest FPGA acceleration card. Knowledge Resources is previewing its KRM-20MSX module at Embedded World 2022, Hall 3A, Booth #335, June 21-23, Nurenberg, Germany.

"Knowledge Resources GmbH has been looking at an opportunity to deploy the BE3-RMW for some time," stated Mike Stengle, CEO of Knowledge Resources. "By designing our new KRM-20 Module with the BE3-RMW, we have created an easy to deploy companion System on Module (SoM) for our high-end FPGA module families."

Knowledge Resources chose Peraso's BE3-RMW for its multi-level, high-performance SRAM memory with embedded In-Memory BURST functions for speed and ease-of-use. The solution combines a high-speed, serial protocol I/O interface with 1.1Gb of high access rate memory in a single package to further accelerate the high-performance Versal Premium VP1202 FPGA from Xilinx to enable a host of applications requiring high performance.

"Peraso is delighted that Knowledge Resources has chosen our proven memory technology," stated Ron Glibbery, CEO of Peraso. "The BE-3 Accelerator Engine is designed to interface the memory to the Xilinx FPGA using 16 SerDes lanes to transmit data at up to 25Gbps, full duplex throughput, enabling up to 5 billion memory transaction per second. This is yet another example of the many customers who have designed in our high access rate memory ICs to accelerate their FPGA designs and offer more flexible and higher performance solutions."

Key features of the BE-3 RMW include:

- 1.152 Gb memory with SerDes I/O
  - o Accessible through as few as 4 Lanes and as many as 16

- Deterministic Latency
- Embedded In-Memory Functions
  - Burst 2, 4 or 8 Simultaneous Read/Writes
  - On chip ALU for RMW (Read/Modify/Write) functions enabling stats, counters and metering to be offloaded from host FPGA
- Two Separate Access Ports
  - Usable as a standard memory
  - Usable as a dual port between 2 FPGAs

For additional information on Knowledge Resources products: LINK

For additional information on Peraso's Blazar Products: LINK

About Peraso, Inc.
Peraso Inc. (NASDAQ:PRSO) is a pioneer in high performance 5G mmWave wireless technology, offering chipsets, modules, software and IP. Peraso supports a variety of applications, including fixed wireless access, immersive video and factory automation. In addition, Peraso's solutions for data and telecom networks focus on Accelerating Data Intelligence and Multi-Access Edge Computing, providing end-to-end solutions from the edge to the centralized core and into the cloud. MoSys, Inc. became Peraso Inc. in December 2021. For additional information, please visit www.perasoinc.com.

Forward-Looking Statements
This press release contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933, as amended, and Section 21E of the Securities Exchange Act of 1934, as amended, which are intended to be covered by the "safe harbor" created by those sections. All statements in this release that are not based on historical fact are "forward-looking statements." These statements may be identified by words such as "estimates," "anticipates," "projects," "plans," "strategy," "goal," or "planned," "seeks," "may," "might", "will," "expects," "intends," "believes," "should," and similar expressions, or the negative versions thereof, and which also may be identified by their context. All statements that address availability, operating performance, anticipated use and advantages of the products of Peraso s, that are not otherwise historical facts, are forward-looking statements.

Forward-looking statements are based on certain assumptions and expectations of future events that are subject to risks and uncertainties. Actual results and trends may differ materially from historical results or those projected in any such forward-looking statements depending on a variety of factors. These factors include, but are not limited to, customers' ability to successfully commercialize products that incorporate our technology, the availability and performance of Peraso's products, reliance on manufacturing partners to assist successfully with the fabrication of our ICs and modules, availability of quantities of ICs supplied by our manufacturing partners at a competitive cost, level of intellectual property protection provided by our patents, vigor and growth of markets served by our customers and our operations, and other risks. Peraso undertakes no obligation to update publicly any forward-looking statement for any reason, except as required by law, even as new information becomes available or other events occur in the future.

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